

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 11-04-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Structure and Method for
Suppressing Sub-Threshold Leakage
in Integrated Circuits
Attorney Docket Number:: X-1266 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 13
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Tim
Family Name:: Tuan
Street:: 3286 Payne Avenue
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95117

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124